Data Sheet

SYNOPSYS®

Saber

Industry Standard for Multi-Technology and Mixed-Signal Simulation

Overview

To solve difficult mixed-signal design and verification problems, Synopsys' Saber® simulator offers a powerful mixed-signal behavioral simulator that efficiently handles architectural exploration through detailed design verification.

Introduced in 1987, Saber was developed by a team of simulation specialists, expert in both mathematical simulation and hardware design. Saber changed the status quo of analog simulation with a structure that supports both a hardware description language and a single-kernel mixed-signal simulation solution.

Application of improved solution methods and algorithms produced a faster, more robust simulator that is more widely applicable than competitive products. Saber continually raises the standard for analog, mixed-signal and multi-technology simulation.

Benefits

- Analyze complete system-on-chip (SoC) designs to large systems that include electrical, mechanical, hydraulic, software-controlled systems, and other technologies.
- Control simulation through an intuitive, graphical user interface.
- Provide highly accurate and efficient results via single-kernel simulation of mixed-signal circuits or systems.
- Examine performance using steady-state, time, frequency, statistical quality, reliability and controls analyses.
- Execute in popular electronic-design-automation (EDA) environments for interoperability, common modeling language, information sharing, and standard library support.

Introduction

Saber simulator is a mathematical engine that solves the network of equations represented by models and their interconnections in a circuit or system. Simulator access is via a highly interactive and easy to use graphical user interface for analyzing designs, operating the simulator, and obtaining and viewing results. If you are primarily concerned with simulating a design, responsible for building the most accurate and comprehensive models, or just working in related applications, Saber simulator provides the simulation capabilities, features, and accuracy to complete the job accurately and on time.

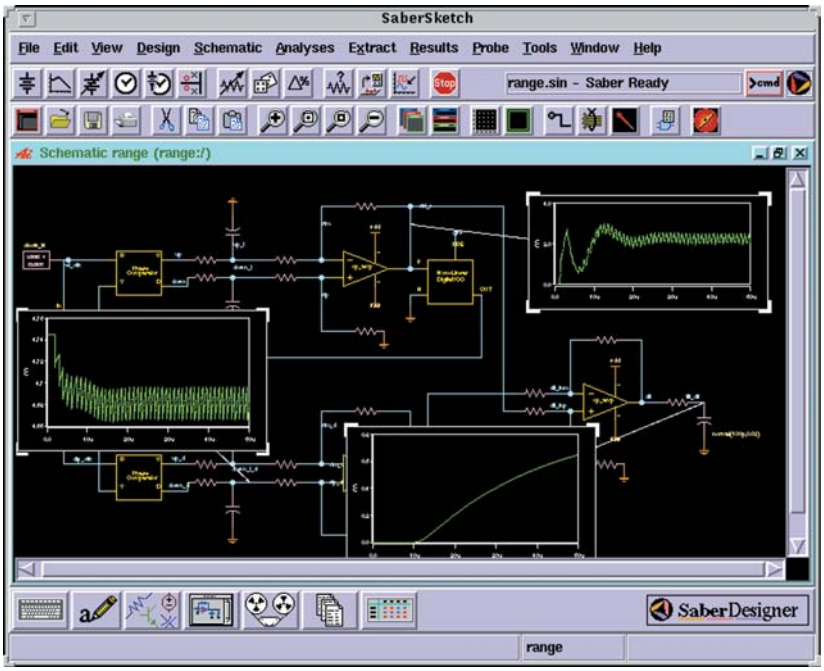

Figure 1:
Saber Sketch is used for schematic entry and simulation control. The simulated results can be displayed in context with the circuit in 'probes'.

Mixed-Signal Functionality

Saber simulator is a full-fledged, single-kernel, mixed-signal simulator. A built-in event algorithm, (coupled with Synopsys MAST® full mixed-signal hardware-description language (HDL) and continuous-time, differential-equation algorithms) simultaneously accommodates event processing, Boolean logic, and continuous mathematical expressions and relationships.

Saber simulator can simulate analog, event-driven analog (such as Z-domain), digital, and mixed-analog/digital devices in the same simulation, while allowing interaction between analog and digital domains.

To effect this linkage between analog and digital algorithms, Synopsys developed the patented Calaveras Algorithm. The Calaveras algorithm allows both simulation methods to operate at maximum efficiency, exchanging information only when required. For simulating designs that include models written in Verilog or VHDL, Saber simulator can be linked with popular digital simulators such as Cadence's Verilog-XL™, Model Technology's ModelSim and ModelSim Plus, and Innoveda's Fusion simulator. Because Saber is inherently a mixed-signal simulator, the co-simulation connection is made between Saber's digital engine and the partner simulator.

Multi-Technology Simulation

Saber simulator is designed to perform simulations based on very few preconceptions about the target system. Consequently, the simulator can analyze designs containing multiple technologies, using the analysis units native to these technologies:

- Electronic
- Power electronics
- Electro-mechanical
- Mechanical
- Electro-optical
- Optical
- Hydraulic
- Control systems
- Sampled-data systems

If device behavior can be expressed in mathematical terms, Saber simulator can model and simulate it – up to the system level – with an accurate representation of interactivity between the technologies. Given this capability, models can be created directly using the actual equations and relationships that govern the behavior of devices, not electrical macromodel equivalents. With Saber simulator, any mix of technologies can be simulated, and all simulation results will be output in the corresponding units.

Robust Convergence Analysis

Saber's solution algorithms were carefully chosen to minimize the possibility of encountering convergence problems – the inability of the simulator to arrive at a mathematical solution – that occur in all simulators. Saber simulator uses multiple robust-solution algorithms, sequentially, to solve convergence problems. During system evaluation, Saber simulator performs a piece-wise-linear evaluation of the exact system of equations, with the resulting linear system being solved exactly. Difficult simulation problems, such as sharp signal transitions during transient analysis, are managed successfully.

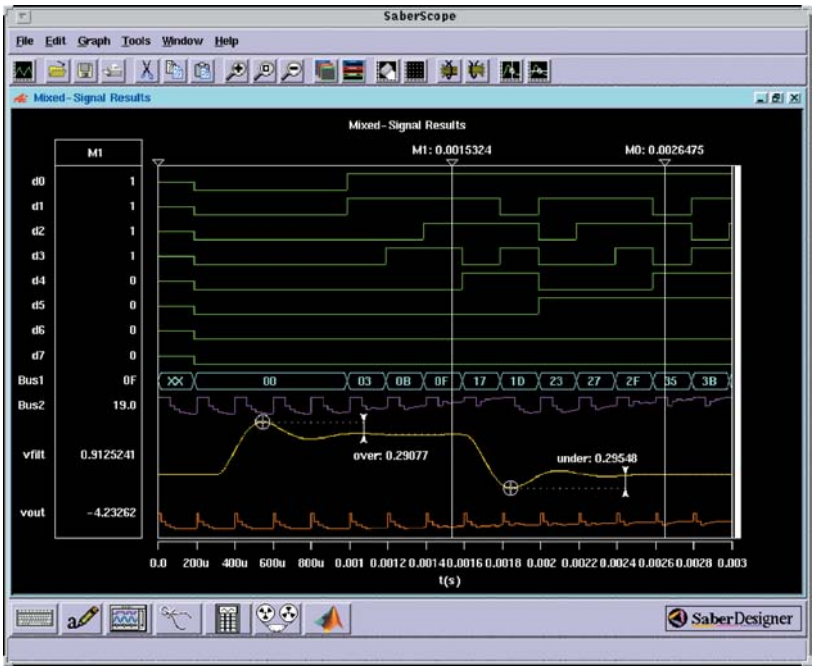

Figure 2:
Saber is a true mixed-signal HDL-based simulator. No co-simulation is required to simulate complex mixed-signal systems.

Simulation Accuracy

When simulation is performed, an engineer wants confidence that results will accurately reflect the operation of the physical system. Based on Synopsys' design and simulation experience, Saber's default accuracy controls are balanced to provide highly accurate results with acceptable computation run times.

Models Separate from Simulator

With Saber, the simulation models are separate from the simulator but are able to control the simulator. This unique configuration allows full access to the models and the simulator at the same time to achieve the best simulation performance.

A design engineer can view the contents of a model, make changes to it, or use it as the starting point for another model. The engineer can create new models and add them to libraries–or start a new library. Design engineers can use the same language (MAST) that Synopsys employs to develop models. Moreover, an engineer can add models or subroutines written in C, C++ and FORTRAN.

Synopsys offers optional model libraries containing thousands of models for designing ICs to complete high-power embedded control systems. These libraries include highly accurate silicon models from Synopsys industry-standard HSPICE® analog simulator as well as high-power silicon devices such as IGBTs and programmable S and Z-domain models for communications systems design.

Support for Popular CAE Frameworks

Saber operates within the Saber® Designer graphical environment and can be integrated into design environments offered by Cadence Design Systems, Mentor Graphics, and Innoveda by employing Synopsys Frameway Integration technology. A design engineer gains full and easy access to all features and capabilities of the Saber simulator even when accessed through the host provider's graphical environment.

Comprehensive Analysis Support

Because Saber is a mixed-signal HDL-based simulator it handles statistical and other analyses that are difficult or impossible with other solutions. When combined with HDL-based models, Saber and InSpecs® enable statistical analysis on any model parameters. The OpAmp input-offset voltage is a simple example of a parameter that is easily varied in Saber with a MAST HDL template but is difficult or impossible to vary with SPICE macromodeling techniques. The ability to vary digital parameters such as delay in a Monte-Carlo analysis is another example.

Saber simulator supports all the standard analog simulation analyses — DC operating point, transient and AC, noise, distortion, and Fourier spectral analysis. For more detailed investigations, Saber and the InSpecs family of design analysis products provide the ability to perform Monte Carlo, stress, sensitivity, and parametric analysis. All analyses can be performed on systems containing any mix of technologies.

View the Possibilities

Generating simulation data is only one facet of successful system analysis. Within the Saber Designer environment, the Saber® Scope graphical waveform analyzer allows easy-to-use viewing and analysis of results. Saber simulator creates a default results file in which a design engineer can specify the particular simulation results to extract. Saber Scope allows the designer to view signals and parameters deep within the hierarchy of a system or model, or to take a quick glance at primary waveforms. If additional details are needed, the engineer can extract them without rerunning the simulation. This unique feature saves simulation time and makes it easy to zero in on the data most important to a particular task.

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Platform Support

- Sun Solaris 8
- HPUX 11
- Windows NT 4.0/2000 and XP Professional
- IBM AIX 4.3.2
- Red Hat Linux 7.2

For more information about Synopsys products, support services or training, visit us on the web at www.synopsys.com, contact your local sales representative or call 650.584.5000.

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