Welcome to the Synopsys University Program September update. This monthly issue provides information on the latest program updates, available curriculum, presentations and activities.

Curriculum

One of the benefits of being a Synopsys University Program member is access to IC design courseware. The following new courses are now available.

Workshops and Lectures:

- **TCAD Course (San Jose State University)**
- **TCAD Quick Start Guide â€“ Updated!**

Short Lectures and Labs:

- **Basic Perl Programming**
- **Multi-threaded SPARC Core Verification Using SystemVerilog Testbench**
- **Process Variation Aware Design**

Visit [Members Only](#) with your SolvNet ID and password for a complete list of courses to download.

Presentations

Didn't make it to the DAC University Reception? Not to worry! View keynote presentations online.

**Addressing Verification Challenges in the Next Decade**
Janick Bergeron
Synopsys Inc.

**Design in the Nano-Scale Era: Low-Power, Reliability and Error Resiliency**
Kaushik Roy
Purdue University

**SRC/NSF Joint Program on Multicore Design and Architecture Program**
Bill Joyner and Sankar Basu
Semiconductor Research Corporation, National Science Foundation

Events

- **Embedded Systems Week**
  Oct 11-16, 2009
  [Register Now!](#)

- **SIUIS 4**
  Oct 29-31, 2009
  [Register Now!](#)

- **ITC 2009**
  Nov 1-6, 2009
  [Register Now!](#)

Call for Papers

- **ASEE 2010**
  Oct 9, 2009
  [Submit Now!](#)

- **IEEE Mini-Grants**
  Oct 15, 2009
  [Submit Now!](#)

- **SNUG San Jose**
  Oct 30, 2009
  [Submit Now!](#)

- **DAC 2010**
  Nov 9, 2009
  [Submit Now!](#)

System Studio Papers

- Technical Paper: Speeding up System Studio Simulations
  [View](#)

  [View](#)

University Program

For additional information on the Synopsys University Program please [contact us](#).