Synopsys University Program

June 2009 Update

Welcome to the Synopsys University Program June update. This monthly issue highlights university events and ISQED conference proceedings.

Going to the Design Automation Conference (DAC) this year? Need to register? We have free exhibit-only registration passes for the first 20 respondents to contact us!

We hope you will join us for the following Synopsys sponsored events co-located with DAC. Register now to reserve your seat for these university events!

Synopsys/Sun University Reception at DAC

Monday July 27, 6:00-8:00pm
Marriott Hotel, Yerba Buena, Salon 7
San Francisco, CA

University professors and students are invited to join Synopsys and Sun Microsystems for an evening reception, including drinks and hors d'oeuvres. Keynote presentations will be featured and prize drawings will be held throughout the evening. Register now!

MSE 2009

July 25-27, 2009
Marriott Hotel, Sierra H
San Francisco, CA

Synopsys is a sponsor of the annual Microelectronics Systems Education Conference (MSE). This conference provides an excellent opportunity for educators and industry to work together to ensure continued excellence in the field of microelectronic systems. Synopsys will be presenting low power tutorials on Saturday, July 25 from 1:30-5:00pm. To attend these tutorials, register for this conference now!

ISQED 2009 Proceedings

Didn't make it to the International Symposium on Quality Electronic Design (ISQED)? Not to worry! View conference papers and presentations online.

- Synopsys Tools Cited in ISQED 2009 Papers
  Various Authors
  ISQED 2009

Presentations:

- **Design & Verification of Low Power SoCs**
  Yatin Trivedi, Gary Delp, John Biggs, Srikanth Jadcherla
  Synopsys Inc., LSI, ARM

- **Functional Verification Planning and Management: Quantifying the Path from Specification to Fully Functional Design**
  Shankar Hemmady and Badri Gopalan
  Synopsys Inc.

- **Impact of SoC Power Management Techniques on Verification and Testing**
  Bhanu Kapoor, Shankar Hemmady, Shreesh Verma, Kaushik Roy
  Mimasic, Synopsys Inc., Conexant Systems, Purdue University

- **Improving Design Quality by Managing Process Variability**
  Terry Ma
  Synopsys Inc.

- **Low Power and Robust Logic Design**
  Swarup Bhunia
  Case Western Reserve University

- **Robust SRAMs in sub-45nm Technology**
  Kaushik Roy
  Purdue University

- **Validating Physical Access Layer of WiMAX Using System Verilog**
  Albert Chiang, Wei-Hua Han, Bhanu Kappor
  Synopsys Inc., Mimasic

For additional information on the Synopsys University Program please contact us.