**ASIP Designer University Program Application Form - Europractice**

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| 1. **Site and Contact Information**

Europractice Member Number \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ and Synopsys SiteID \_\_\_\_\_\_\_\_\_\_\_\_\_Organization Name: Address:   Technical Contact name: email: telephone:  |

1. **Server and License Information**

ASIP Designer licenses may only be added to a license server that has existing Front End and Verification (FEV) Licenses. Please provide details of your FEV license server below

|  |  |  |
| --- | --- | --- |
| **Number of FEV licenses** | **License Server Host ID** | **License Server O/S** |
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1. **Licensed Products**

|  |  |  |
| --- | --- | --- |
| **Product Name** | **Product Code** | **License Quantity** |
| ASIP Designer University Bundle | I531-0 | As FEV Quantity Above |

1. **Status/Activities Information**

Please describe your academic use of the Synopsys Licensed Product (ASIP Designer). **Note:** Use must be non-commercial in nature and adhere to the terms and conditions of the End User license agreement already signed for use of Synopsys tools via Europractice

Complete all relevant fields and provide full details, expanding the sections as necessary

**Course(s):** *Duplicate and Step-and-repeat the following section for each course/module.*

|  |  |
| --- | --- |
| Course Name |  |
| Instructor/Professor(s) |  |
| Course URL |  |
| Description |  |
| Course Level |  Undergraduate  Graduate Masters  Graduate PhD  Other:  |
| Synopsys tools planned to be used | ASIP Designer |
| Number of students attending |  |

**Research Project(s):** *Duplicate and Step-and-repeat the following section for each research project.*

|  |  |
| --- | --- |
| Project Name |  |
| Instructor/Professor(s)  |  |
| Project URL |  |
| Description |  |
| Synopsys tools planned to be used | ASIP Designer |
| Number of professors/students participating |  |
| Who is sponsoring the research project - (internal and/or external-entity sponsor(s))? |  |
| What are the research project deliverables? |  |

**Technical Paper(s):** *Step-and-repeat the following section for each published paper.*

|  |  |
| --- | --- |
| Paper Title |  |
| Synopsys tools referenced |  |
| Published URL |  |

**Other:** *List any other activities (present and future) based on use of the Synopsys Licensed Product.*

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I acknowledge that approval of this request to participate in the ASIP Designer University Program is at Synopsys’ sole discretion. Any ASIP Designer Licenses provided will be issued on the same server as existing Europractice Front End and Verification (FEV) licenses and use of ASIP Designer licenses will be also governed by the Europractice-Synopsys End User License Agreement already in place for these FEV licenses. ASIP Designer licenses will be synchronized to the same cycle as FEV licenses and I understand that should I terminate use of (or fail to pay invoiced fees for) Europractice Front End and Verification (FEV) licenses, that ASIP Designer Licenses will also terminate.

I agree that participation in the ASIP Designer University Program required me to

* deliver an Annual Report to Synopsys, summarizing the activities performed and the achieved results
* refer to Synopsys in publications (if any) and inform Synopsys at time of submission and of acceptance
* participate in ASIP University Days where possible

I request to participate in the ASIP Designer University Program.

Name: Organization Stamp

Position:

Signature:

Date:

Once completed, please return this document to Europractice at the Microelectronics Support Centre (address below) who will then forward this to Synopsys for consideration. If approved, ASIP Designer licenses will then be delivered via Europractice using the regular monthly order cycle for new licenses.

Microelectronics Support Centre, STFC Rutherford Appleton Laboratory, Didcot, OX11 0QX, United Kingdom

MicroelectronicsCentre@stfc.ac.uk